

<b>Notice of References Cited</b>	Application/Control No. 09/498,677		Applicant(s)/Patent Under Reexamination STEINHOFF ET AL.	
	Examiner Johannes P. Mondt		Art Unit 3663	Page 1 of 1

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Anderson et al, "ESD Protection for Mixed-Voltage I/O Using NMOS Transistors Stacked in a Cascode Configuration", Electrical Overstress / Electrostatic Discharge Symposium, October 6-8, 1998.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.